

60MHz/100MHz Digital Storage Oscilloscope

NEW



**GDS-806S(60MHz)
GDS-810S(100MHz)**

NEW



**GDS-806C(60MHz)
GDS-810C(100MHz)**

GDS-806/810 series 60MHz/100MHz DSO with color or monochrome LCD display, sets a new price/performance standard for a full-featured Digital Storage Oscilloscope in the market. With an affordable price, GDS-806/810 Series makes the idea of a personal DSO possible. There is no need for a group of engineers or service technicians to share an expensive DSO due to the cost consideration. Each student in the lab could now enjoy playing with his own DSO to get a better learning result from the course. The manufacturing plant has a most cost-effective solution for DSO requirements. The ATE system integrator is pleased to find an economic DSO component without degrading any functions or performance of the system. GDS-806/810 Series is available for the market, which needs a full-featured DSO at an affordable price without the sacrifice of performance.

FEATURES

- * 60MHz/100MHz Bandwidth With Either Color or Monochrome LCD Display
- * 125k Long Memory and 12 Division Horizontal Display
- * 25GS/s Sampling Rate for Repetitive Waveforms
- * Advanced Trigger : Pulse Width , TV Line, Event Delay and Time Delay
- * Go/NoGo and Auto Setup Sequence
- * FFT Function
- * Built-In Help Menu, Multi-Language and PC Software
- * Standard Interface: RS-232C
- * Option: GPIB, Printer Port and USB Interface

SPECIFICATIONS

		GDS-806S	GDS-806C	GDS-810S	GDS-810C
DISPLAY SYSTEM	Display Device	Mono (320 x 240) 5.7 inch LCD	Color (320 x 240) 5.7 inch LCD	Mono (320 x 240) 5.7 inch LCD	Color (320 x 240) 5.7 inch LCD
	Display Contrast Waveform Display Graticule Display Mode	Adjustable 8 x 10 divisions, (8 x 12 div, when menu off) Dot, Vector, Accumulate			
VERTICAL SYSTEM	Bandwidth	60MHz		100MHz	
	Channels	2			
	Vertical Resolution	8-Bit			
	Vertical Sensitivity	2mV/div ~ 5V/div			
	Vertical Accuracy	± 3%			
	Rise Time	< 5.8ns		< 3.5ns	
	Input Coupling Input Impedance Polarity Maximum Voltage Between Signal & Common at Input BNC Waveform Signal Process Offset Range BW Limit	AC、DC、Ground 1MΩ ± 2%, ~18pF Positive & Negative 300V (DC+AC peak), CATII CH1+CH2、CH1-CH2、FFT 2mV/div ~ 50mV/div : ±0.5V; 100mV/div ~ 500mV/div : ±5V; 1V/div ~ 5V/div : ±50V 20MHz (-3dB)			
HORIZONTAL SYSTEM	Time Base Range	1ns/div ~ 10s/div (1-2-5 increments)			
	Time Base Mode	Main、Window、Window Zoom、Roll、X-Y			
	Time Base Accuracy Delay Range	± 0.01% Pre-trigger : 20 div maximum; Post-trigger : 1000 div			
SIGNAL ACQUISITION SYSTEM	Real-Time Sample Rate	100MS/s maximum on each channel			
	Equivalent Sample Rate	25GS/s E.T. maximum on each channel			
	Record Length	125k/CH			
	Peak Detection Acquisition Mode Average	10ns (500ns/div ~ 10s/div) Sample、Peak Detect、Average 2, 4, 8, 16, 32, 64, 128, 256			
TRIGGER	Trigger Source Mode Coupling Sensitivity	CH1, CH2, Line, Ext Auto Level、Auto、Normal、Single、TV、Time Delay、Event Delay、Edge、Pulse Width AC, DC, HF, LF, Noise Reject DC~25MHz : Approx 0.5div or 5mV 25MHz~60MHz : Approx 1.5div or 15mV			
				DC~25MHz : Approx 0.5div or 5mV 25MHz~100MHz : Approx 1.5div or 15mV	
X-Y MODE	X-Axis Input / Y-Axis Input Phase Shift	Channel 1 / Channel 2 ± 3° at 100kHz			
CURSOR & MEASUREMENT	Auto Voltage Measurement Auto Time Measurement Cursor Measurement	$V_{pp}, V_{amp}, V_{avg}, V_{rms}, V_{hi}, V_{lo}, V_{max}, V_{min}$ Freq, Period, Rise Time, Fall Time, Positive Width, Negative Width, Duty Cycle Voltage difference between cursors (ΔV) Time difference between cursors (ΔT) Reciprocal of ΔT in hertz (1/ΔT)			